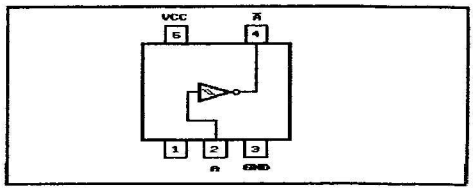
## 4 ...ngle-Gate Schmitt-Trigger SMD

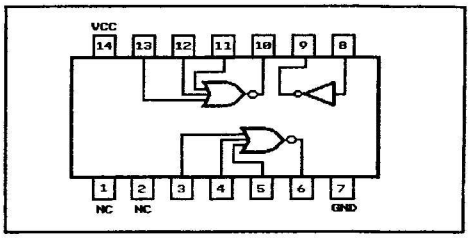
...4584

| | | Prix 1... | 25... |
|---|---|---|---|
| | ...S584F - SO | 0.60 | 0.55 |




## 4 ...nput NOR Gate Plus Inverter

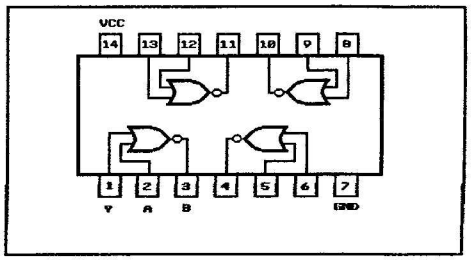
| N | | Prix 1... | 25... |
|---|---|---|---|
| 1 | ...000B | 0.55 | 0.45 |




## 4 ...UB Quad 2-Input NOR Gate

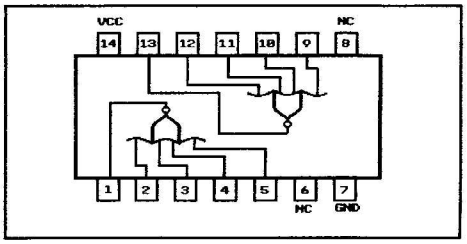
T... ...ffered version of the 4000B. It is therefore ideal for analogue applications.

| N | | Prix 1... | 25... |
|---|---|---|---|
| 314... | 4001B | 0.30 | 0.25 |
| 313... | 4001B / SMD-SO14 | 0.30 | 0.25 |
| 162... | 4001UB | 0.50 | 0.45 |




## 40... ...4-Input NOR Gate

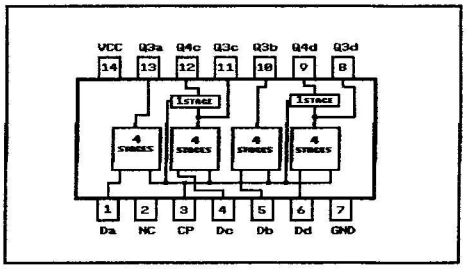
| N°d'... | | Prix 1... | 25... |
|---|---|---|---|
| 3140... | 4002B | 0.30 | 0.25 |




## 400... ...Stage Static Shift Register

The 4... ...es four separated shift registers controlled by a common clock. Two sections have f... ...nd two sections have five stages with a additional output after the fourth stage. Thus is pos... ...cting appropriate stages, to make shift registers of length 4,5,8,9,10,12,13,14,16,17 and 18...

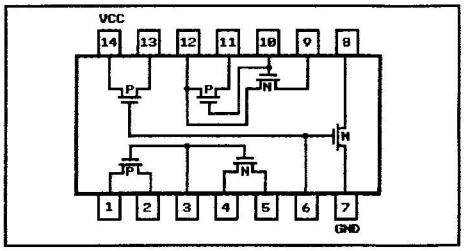
| N°d'ar... | | Prix 1... | 25... |
|---|---|---|---|
| 31402 | 4006B | 1.30 | 1.00 |




## 4007... ...ual Complementary Pair Plus Inverter

This ve... ...useful in inverter circuits, pulse shapers, linear amplifiers, high input impedance amplifi... ...detectors, transmission gating and functional gating.

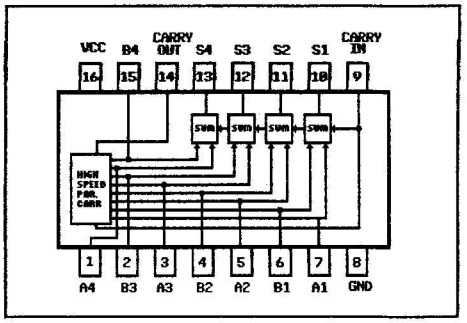
| N°d'ar... | | Prix 1... | 25... |
|---|---|---|---|
| 31403 | 4007UB | 0.30 | 0.25 |




## 4008... ...Full Adder

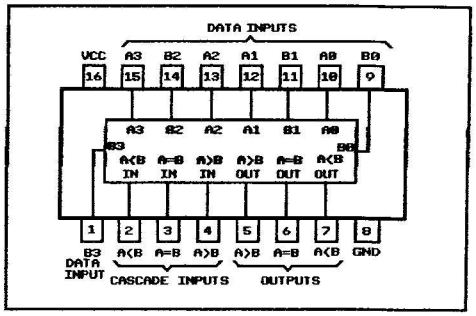
The 4... ...gether two four bit binary numbers and generate a carry if applicable. A fast interna... ...allows the carry to be generated very quickly keeping the total summing time relative... ...when large numbers of these devices are cascaded. To connect together, simply join the... ...stage handling less significant bits to the carry input of the next stage handling more s... ...The carry input of the least significant device and where only one is in use, must be con...

| N°d'ar... | | Prix 1... | 25... |
|---|---|---|---|
| 31404 | 4008B | 1.00 | 0.80 |



## 4063B 4-Bit Magnitude Comparator

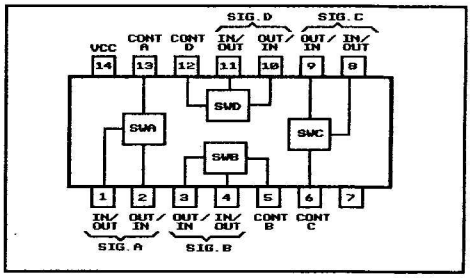
This IC determines whether the binary code on the four 'A' inputs is greater than, equal to, or smaller than the binary code on the four 'B' inputs. A separate output is available for each possible condition. Words of greater length may be compared by simply connecting the corresponding outputs on a stage handling less significant bits to the cascade inputs of the next stage handling more significant bits. The final output comes from the most significant comparator. On the least significant comparator and where only one comparator is in use, the A=B cascade input must be connected to logic 1 and the other two cascade inputs to logic 0.




| N°d'art. | | Prix 1... | 25... |
|---|---|---|---|
| **16217** | 4063B | 0.95 | 0.75 |

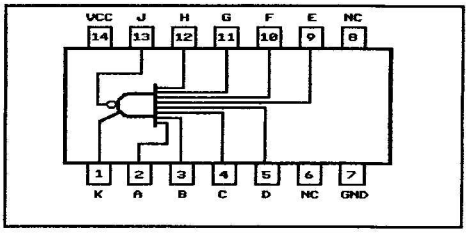
## 4066B Quad Bilateral Switch

The 4066B is a quad bilateral switch intended for the transmission or multiplexing of analog or digital signals. It is pin-for-pin compatible with the 4016B, but has a much lower "ON" resistance, and "ON" resistance is relatively constant over the input-signal range.



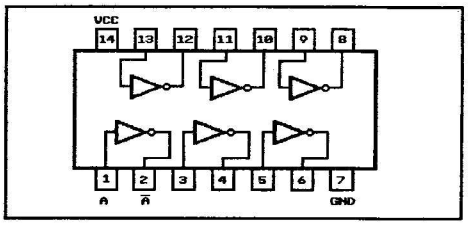

| N°d'art. | | Prix 1... | 25... |
|---|---|---|---|
| **16218** | 4066B | 0.35 | 0.30 |
| **92066** | 4066B / SMD | 0.30 | 0.25 |

## 4068B 8-Input NAND Gate



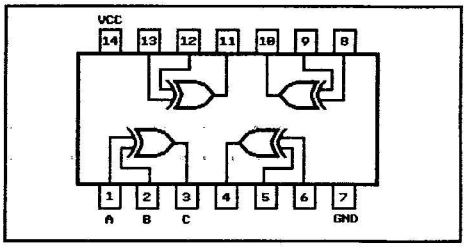

| N°d'art. | | Prix 1... | 25... |
|---|---|---|---|
| **16219** | 4068B | 0.40 | 0.35 |

## 4069UB Hex Inverter



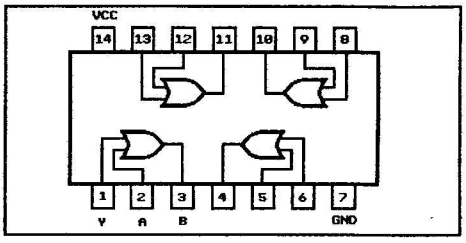

| N°d'art. | | Prix 1... | 25... |
|---|---|---|---|
| **31443** | 4069UB | 0.35 | 0.30 |
| **92069** | 4069UB / SMD | 0.35 | 0.30 |

## 4070B QUAD Exclusive-OR



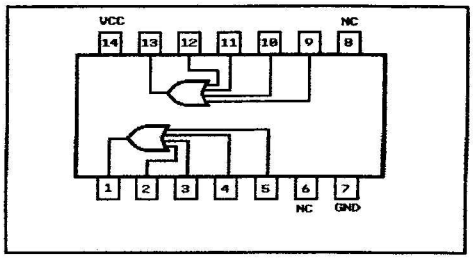

| N°d'art. | | Prix 1... | 25... |
|---|---|---|---|
| **31444** | 4070B | 0.30 | 0.20 |

## 4071B Quad 2-Input OR Gate



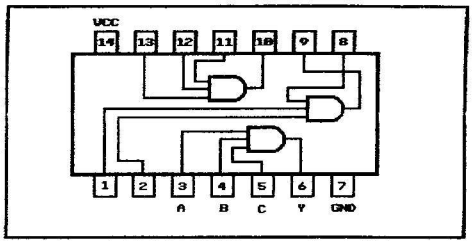

| N°d'art. | | Prix 1... | 25... |
|---|---|---|---|
| **31445** | 4071B | 0.30 | 0.25 |
| **31450** | 4071B / SMD-SO14 | 0.30 | 0.25 |

## 4072B Dual 4-Input OR Gate



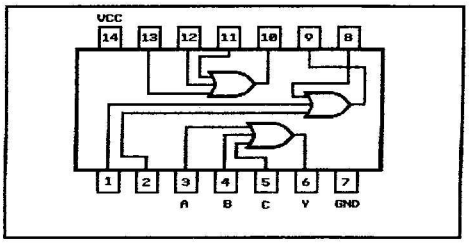

| N°d'art. | | Prix 1... | 25... |
|---|---|---|---|
| **31446** | 4072B | 0.30 | 0.25 |

## 4073B Triple 3-Input AND Gate




| N°d'art. | | Prix 1... | 25... |
|---|---|---|---|
| **31447** | 4073B | 0.30 | 0.25 |

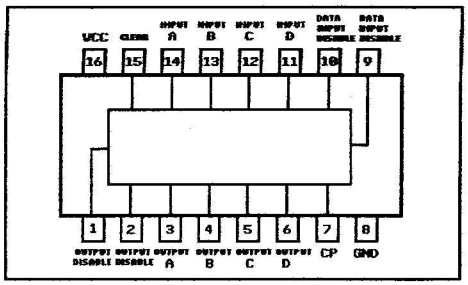
## 4075B Triple 3-Input OR Gate




| N°d'art. | | Prix 1... | 25... |
|---|---|---|---|
| **31448** | 4075B | 0.30 | 0.25 |

## 4076B Quad D Flip-Flop with 3-State Outputs

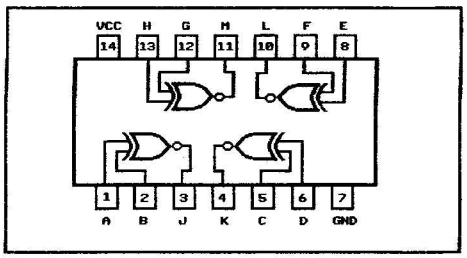
The 4076 contains four D-type positives-edge-triggered flip-flops with three states outputs. Gated enable inputs control, the entry of data into the flip-flops. When both pins 9 and 10 are low, data is loaded on the next positive clock transition. When pins 1 and 2 are both low, the outputs function normally, but either or both are high, the outputs present a high impedance. A reset, pin 15, is also provided.




| N°d'art. | | Prix 1... | 25... |
|---|---|---|---|
| **31449** | 4076B | 0.85 | 0.70 |

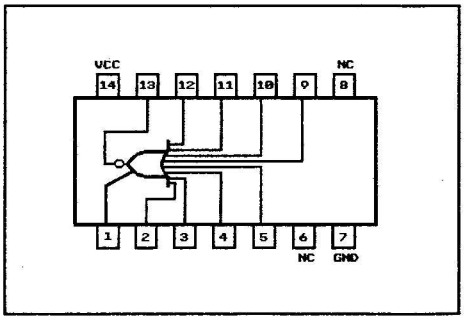
## 4077B Quad Exclusive-NOR Gate

The 4077B may be used interchangeably for the 4811.




| N°d'art. | | Prix 1... | 25... |
|---|---|---|---|
| **16221** | 4077B | 0.30 | 0.25 |

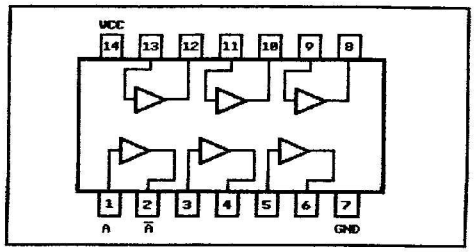
## 4078B 8-Input OR/NOR Gate




| N°d'art. | | Prix 1... | 25... |
|---|---|---|---|
| **16223** | 4078B | 0.40 | 0.35 |

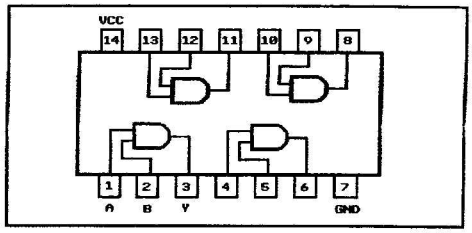
## 7407 Hex Buffer Driver with Open-Drain Outputs (30V)

This device contains hex non-inverting buffers with open-collectors. The 74LS07 output can handle voltages up to 30V.



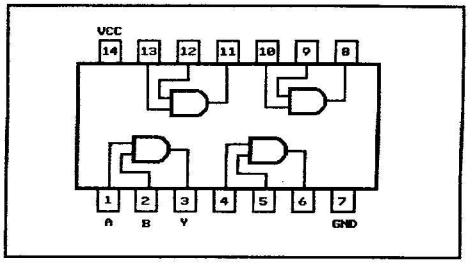

| N°d'art. | | Prix 1... | 25... |
|---|---|---|---|
| **15512** | 7407N | 0.70 | 0.50 |
| **15511** | 74LS07 | 1.40 | 1.20 |

## 7408 Quad 2-Input AND Gate




| N°d'art. | | Prix 1... | 25... |
|---|---|---|---|
| **F0180** | 74ACT08 / SMD-SO14 | 0.60 | 0.50 |
| **31006** | 74HC08 | 0.35 | 0.25 |
| **93008** | 74HC08 / SMD-SO14 | 0.40 | 0.30 |
| **F0192*** | 74LCX08 / SMD-SO14 | 0.60 | 0.50 |
| **74908*** | 74LS08 | 0.50 | 0.40 |
| **79573** | NC7S08M5 / Single Gate SMD-SOT23/5 | 0.50 | 0.40 |

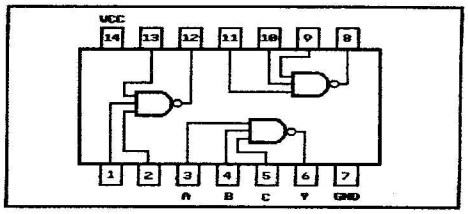
## 7409 Quad 2-Input AND Gate with Open-Drain Outputs




This device contains four independent 2-Input AND Gates. The Open-Drain outputs require pull-up resistors to perform correctly. With suitable pull-up resistors, these devices can be used in active-low wired-OR or active-high wired-AND applications.

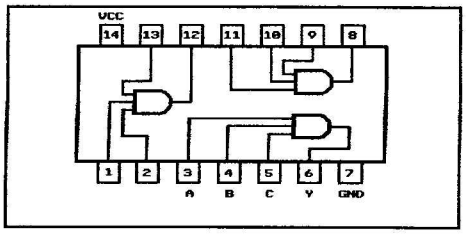
| N°d'art. | | Prix 1... | 25... |
|---|---|---|---|
| **31320** | 74LS09 | 0.60 | 0.50 |

## 7410 Triple 3-Input NAND Gate




| N°d'art. | | Prix 1... | 25... |
|---|---|---|---|
| **73001*** | 74AC10SC / SMD-SO14 | 0.50 | 0.40 |
| **31007** | 74HC10 | 0.30 | 0.25 |
| **75219*** | 74LS10 | 0.50 | 0.40 |

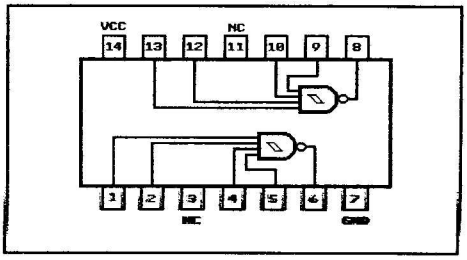
## 7411 Triple 3-Input AND Gate




| N°d'art. | | Prix 1... | 25... |
|---|---|---|---|
| **31008** | 74HC11 | 0.40 | 0.30 |
| **84105** | 74HC11 / SMD-SO14 | 0.40 | 0.30 |
| **91706*** | 74HCT11 | 0.60 | 0.40 |
| **91705*** | 74HCT11 / SMD-SO14 | 0.30 | 0.20 |

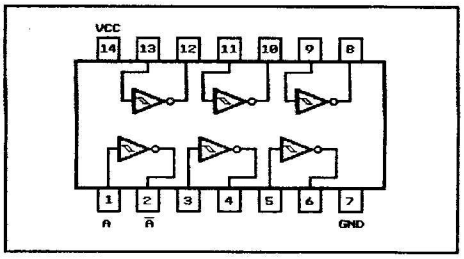
## 7413 Dual 4-Input NAND Schmitt Trigger

The 74LS13 contains two 4-input NAND gates which accepts standard TTL input signals and provide standard TTL output levels. They are capable of transforming slowly changing input signals into sharply defined, jitter-free, output signals. In addition, they have greater noise margin than conventional NAND gates.




| N°d'art. | | Prix 1... | 25... |
|---|---|---|---|
| **91600** | 74LS13 | 0.80 | 0.70 |

## 7414 Hex Inverting Schmitt Trigger




| N°d'art. | | Prix 1... | 25... |
|---|---|---|---|
| **F0190*** | 74AC14 / SMD-SO14 | 0.50 | 0.40 |
| **31009** | 74HC14 | 0.40 | 0.30 |
| **93014** | 74HC14 / SMD-SO14 | 0.30 | 0.25 |
| **93016** | 74HC14PW / SMD-SSOP14 | 0.60 | 0.50 |
| **79897*** | 74AC14 / SMD-SO14 | 0.30 | 0.20 |
| **F0189*** | 74VHC14 / SMD-SO14 | 0.60 | 0.50 |
| **79574** | NC7S14M5 / Single Gate SMD SOT23/5 | 0.50 | 0.40 |

* = Jusqu'à épuisement du stock P = Documentation disponible sur www.cedis.ch